

AMENDMENTS TO THE CLAIMS

(IN REVISED FORMAT COMPLIANT WITH THE PROPOSED
REVISION TO 37 CFR 1.121)

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1. (CURRENTLY AMENDED) An apparatus comprising:
a low speed tester; and
a host emulator having (i) a first interface coupled
between a to said low speed tester to receive a test vector at a
5 first speed, and (ii) a second interface configured to (a) transmit
said test vector to a device having a first at a second speed
faster than a second said first speed of said low speed tester and
(b) receive a response from said device and (iii) a third interface
to said low speed tester to transfer a signal based upon said
10 response, wherein said apparatus is configured to allow said low
speed tester to perform high speed tests of said device at said
first second speed exclusively through said apparatus.

2. (CURRENTLY AMENDED) The apparatus according to claim 1, wherein said ~~low speed tester host emulator~~ is further configured to ~~control high speed transmit, reception and perform a~~ verification of said device.

3. (ORIGINAL) The apparatus according to claim 1, wherein said device comprises a Universal Serial Bus (USB) device.

4. (CURRENTLY AMENDED) The apparatus according to claim 1, further comprising:

~~a host emulator configured to interface with said device,~~

and

5 a test vector generator configured to transfer said test vector to said low speed tester.

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5. (ORIGINAL) The apparatus according to claim 4, wherein said low speed tester is configured to control said host emulator.

6. (CURRENTLY AMENDED) The apparatus according to claim 4, wherein said low speed tester is configured in response to ~~one or more~~ said test vectors vector.

7. (CURRENTLY AMENDED) The apparatus according to claim 6, wherein said test vector generator is configured to generate said ~~one or more~~ test vectors vector.

8. (ORIGINAL) The apparatus according to claim 1, wherein said apparatus is further configured to test a reception and transmission operation of said device.

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Contd.

9. (ORIGINAL) The apparatus according to claim 1, wherein said apparatus is further configured to initiate one or more test packets.

10. (ORIGINAL) The apparatus according to claim 9, wherein said device is further configured to receive and verify said one or more test packets.

11. (CURRENTLY AMENDED) The apparatus according to claim 10 1, wherein said device is further configured to transmit said one or more test packets.

12. (CURRENTLY AMENDED) The apparatus according to claim 4 11, wherein said apparatus host emulator is further configured to receive and verify said one or more transmitted test packets.

13. (ORIGINAL) The apparatus according to claim 1, wherein said low speed tester is further configured to generate a pass/fail signal.

14. (ORIGINAL) The apparatus according to claim 1, wherein said apparatus is configured to perform at least one test of a plurality of test modes wherein said plurality of test modes

comprise USB 2.0 defined test modes for use in a production test
5 environment.

15. (CURRENTLY AMENDED) An apparatus comprising:
~~means for transferring a test vector at testing a device~~
~~having a first speed to a first interface;~~
~~means for transmitting said test vector from a second~~
~~interface to a device at a second speed faster than said first~~
~~speed;~~
~~means for receiving a response from said device at said~~
~~second interface; and~~
~~means for transferring a signal based upon said response~~
10 ~~from a third interface configuring a low speed tester having a~~
~~second speed slower than said first speed to perform high speed~~
~~tests of said device at said first second speed exclusively through~~
~~said testing means.~~

16. (CURRENTLY AMENDED) A method for testing comprising
the steps of:

(A) ~~transferring a test vector at testing a device~~
~~having a first speed with from a low speed tester to a first~~
5 ~~interface of a host emulator; and~~

(B) transmitting said test vector from a second interface of said host emulator at a second speed faster than said first speed to a device;

5 (C) receiving a response from said device at said second interface; and

(D) transferring a signal from a third interface of said host emulator to said configuring a low speed tester having a second speed slower than said first speed based upon said response to perform high speed tests of said device at said first second speed exclusively through said host emulator.
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17. (CURRENTLY AMENDED) The method according to claim 16, wherein said device ~~under test~~ comprises ~~an~~ a USB device.

18. (CURRENTLY AMENDED) The method according to claim 16, ~~wherein step (B) further comprises further comprising the step of:~~

5 configuring said low speed tester to control said host emulator.

19. (CURRENTLY AMENDED) The method according to claim 18, ~~wherein step (B) further comprises further comprising the step of:~~

generating said test vector external to interfacing said
5 host emulator with ~~said device under test.~~

20. (ORIGINAL) The method according to claim 16, further comprising performing at least one of a plurality of test modes wherein the plurality of test modes comprise USB 2.0 defined test modes for use in a production test environment.
